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1	event adj based and cycle adj based
2	event adj based and cycle adj based and normaliz\$6
3	event adj based same cycle adj based same (translat\$4 conver\$5)
4	event adj based same cycle adj based same (translat\$4 transform\$6 conver\$5)
5	event adj based same cycle adj based same (translat\$4 transform\$6 conver\$5) and (device adj under adj test dut)
6	event adj base\$4 same cycle adj base\$4 same (translat\$4 transform\$6 conver\$5) and (device adj under adj test dut) and simulat\$4 and (known stored intentional\$4 injected signature) same (flaw irregular\$5 drift jitter glitch\$4 spike) with (timing clock)
7	event adj base\$4 same cycle adj base\$4 same (translat\$4 transform\$6 conver\$5) and (device adj under adj test dut) and simulat\$4 and (known stored intentional\$4 inject\$4 introduc\$4 signature) same (flaw irregular\$5 drift jitter glitch\$4 spike) with (timing clock)
8	event adj base\$4 same cycle adj base\$4 same (translat\$4 transform\$6 conver\$5) and simulat\$4 and (known stored intentional\$4 inject\$4 introduc\$4 signature) same (flaw irregular\$5 drift jitter glitch\$4 spike) with (timing clock)
9	"6170078".pn.
10	"6170078".pn. and event and cycle
11	event near2 bas\$4 same cycle near2 based same (translat\$4 transform\$6 conver\$5 format) and simulat\$4
12	event near2 bas\$4 same cycle near2 based same (translat\$4 transform\$6 conver\$5 format) and simulat\$4 and cycli\$7
13	event near2 bas\$4 same cycle near2 based same (translat\$4 transform\$6 conver\$5 format) and simulat\$4 and cycli\$7 and @ad<"20040301"
14	(event near2 bas\$4) same test same (format data) same cycle near2 based same test same (data format) same (translat\$4 transform\$6 conver\$5 format) and simulat\$4 and cycli\$7 and @ad<"20040301"
15	"20050193355" and event adj based and cycle adj based
16	"20050193355" and event adj based and cycle adj based
17	"20040236042"
18	"20040236342"
19	"20040166668"
20	(event near2 bas\$4) same test\$4 same (format\$4 reformat\$4 data) same cycle near2 based same test\$4 same (format\$4 reformat\$4 data) same (translat\$4 transform\$6 conver\$5 format) and simulat\$4 and cycli\$7 and @ad<"20040301" and (vcd)
21	(event near2 bas\$4) same test\$4 same (format\$4 reformat\$4 data) same cycle near2 based same test\$4 same (format\$4 reformat\$4 data) same (translat\$4 transform\$6 conver\$5 format) and simulat\$4 and cycli\$7 and @ad<"20040301" and (normaliz6 vcd extract\$4)
22	(event near2 bas\$4) same test\$4 same (format\$4 reformat\$4 data) same cycle near2 based same test\$4 same (format\$4 reformat\$4 data) same (translat\$4 transform\$6 conver\$5 format) and simulat\$4 and cycli\$7 and @ad<"20040301" and (normaliz6 extract\$4)
23	"20050193355" and (jitter drift irregular\$6)

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24	event near2 bas\$4 same cycle near2 bas\$4 same (translat\$4 transform\$6 conver\$5 format\$4 reformat\$4 re adj format\$4) and (vcd simulat\$4) and clock near3 (drift\$4 jitter glitch\$3 spike noise)
25	event near2 bas\$4 same cycl\$7 same (translat\$4 transform\$6 conver\$5 format\$4 reformat\$4 re adj format\$4) and (vcd simulat\$4) and clock near3 (drift\$4 jitter glitch\$3 spike noise)
26	event near2 bas\$4 same cycl\$7 same (translat\$4 transform\$6 conver\$5 format\$4 reformat\$4 re adj format\$4) and (vcd simulat\$4) and timing near3.(drift\$4 jitter glitch\$3 spike noise irregular\$5 uncertain\$5)
27	event near2 bas\$4 same cycl\$7 same (translat\$4 transform\$6 conver\$5 format\$4 reformat\$4 re adj format\$4) and (timing clock) same (drift\$4 jitter glitch\$3 spike noise irregular\$5 uncertain\$5 flaw) same (known stor\$4 inject\$4 reinject\$4 prespecif\$6 predeterm\$4)
28	"20050198601"
29	event same cycl\$7 same (translat\$4 transform\$6 conver\$5 format\$4 reformat\$4 re adj format\$4) and (timing clock) same (drift\$4 jitter glitch\$3 spike noise irregular\$5 uncertain\$5 flaw abnormal\$6) same (known stor\$4 inject\$4 reinject\$4 prespecif\$6 predeterm\$4)
30	event with cycl\$7 with (translat\$4 transform\$6 conver\$5 format\$4 reformat\$4 re adj format\$4) and (timing clock) same (drift\$4 jitter glitch\$3 spike noise irregular\$5 uncertain\$5 flaw abnormal\$6) same (known stor\$4 inject\$4 reinject\$4 prespecif\$6 predeterm\$4).
31	event with cycl\$7 with (translat\$4 transform\$6 conver\$5 format\$4 reformat\$4 re adj format\$4) and (timing clock) same (drift\$4 jitter glitch\$3 spike noise irregular\$5 uncertain\$5 flaw abnormal\$6) same (known stor\$4 inject\$4 reinject\$4 prespecif\$6 predeterm\$4) and cycliz\$7